

80V N-CHANNEL POWER MOSFET

DESCRIPTION:



The ALPB035N08A is an 161A, 80V N-Channel Power MOSFET and it has High Speed Power Switching.

FEATURES:

- $R_{DS(ON) typ} = 3m\Omega$, $I_D (Silicon Limited) = 161A$, $V_{DS} = 80V$
- High Speed Power Switching
- Enhanced Body diode dv/dt capability
- Enhanced Avalanche Ruggedness
- 100% UIS Tested, 100% Rg Tested
- RoHS compliant & halogen-free.
- Suffix "-H" indicated Halogen Free part, ex. ALPB035N08A-H

APPLICATIONS:

- Synchronous Rectification in SMPS.
- Hard Switching and High-Speed Circuit.
- DC/DC in Telecoms and Industrial.

MECHANICAL CHARACTERISTICS

- Epoxy: UL94-V0 rated flame retardant.
- Case: Molded plastic, TO-263
- Terminals: Solder plated, solderable per MIL-STD-750, Method 2026
- Mounting Position: Any.

ORDERING PART NUMBER

PART NUMBER	ORDERING PART NUMBER
ALPB035N08A	ALPB035N08A-SM

MAXIMUM RATINGS

MAXIMUM RATINGS @ $T_A = 25\text{ }^{\circ}\text{C}$ unless otherwise specified				
PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DS}	80	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current (Silicon Limited)	@ $T_C=25^{\circ}\text{C}$	I_D	161	A
	@ $T_C=100^{\circ}\text{C}$		114	
Pulsed Drain Current		I_{DM}	400	A
Single pulse avalanche energy	$L=0.1\text{mH}$, @ $T_C=25^{\circ}\text{C}$	E_{AS}	80	mJ
Power Dissipation	@ $T_C=25^{\circ}\text{C}$	P_D	172	W
Operating Junction Temperature Range		T_J	-55 to +175	$^{\circ}\text{C}$
Storage Temperature Range		T_{STG}	-55 to +175	$^{\circ}\text{C}$
Note: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied. 2. Repetitive rating: pulse width limited by maximum junction temperature. 3. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.				

THERMAL CHARACTERISTICS @ $T_A = 25\text{ }^{\circ}\text{C}$ unless otherwise specified			
PARAMETER	SYMBOL	RATINGS	UNIT
Thermal Resistance Junction to Ambient	$R_{\theta JA}$	60	$^{\circ}\text{C}/\text{W}$
Thermal Resistance Junction to Case	$R_{\theta JC}$	0.87	$^{\circ}\text{C}/\text{W}$

ELECTRICAL CHARACTERISTICS @ TA = 25 °C unless otherwise specified

PARAMETER	CONDITIONS	SYMBOL	MIN	TYP.	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250 \mu A$	$V_{(BR)DSS}$	80			V
Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 80V, T_J = 25^\circ C$	I_{DSS}			1.0	μA
	$V_{GS} = 0V, V_{DS} = 80V, T_J = 100^\circ C$				100	
Gate-source leakage current	$V_{GS} = \pm 20V, V_{DS} = 0V$	I_{GSS}			± 100	nA
ON CHARACTERISTICS						
Gate-Threshold Voltage	$V_{GS} = V_{DS}, I_D = 250 \mu A$	$V_{GS(TH)}$	2.0	2.7	4.0	V
Drain-to-Source On-Resistance	$V_{GS} = 10V, I_D = 20A$	$R_{DS(ON)}$		3.0	3.5	m Ω
Transconductance	$V_{DS} = 5V, I_D = 20A$	g_{fs}		65		S
Gate Resistance	$V_{GS} = 0V, V_{DS}$ Open, $f = 1MHz$	R_G		1.20		Ω

SWITCHING PARAMETERS						
PARAMETER	CONDITIONS	SYMBOL	MIN	TYP.	MAX	UNIT
Input Capacitance	$V_{GS} = 0V, V_{DS} = 40V, f = 1MHz$	C_{iss}		4347		pF
Output Capacitance		C_{oss}		703		
Reverse Transfer Capacitance		C_{rss}		28		
Total Gate Charge	$V_{DD} = 40V, I_D = 20A, V_{GS} = 10V$	Q_g		68		nC
Gate to Source Charge		Q_{gs}		13		
Gate to Drain (Miller) Charge		Q_{gd}		17		
Turn-On Delay Time	$V_{DD} = 40V, I_D = 20A, V_{GS} = 10V, R_G = 10\Omega$	$t_{d(on)}$		15		nS
Rise time		t_r		12		
Turn-Off Delay Time		$t_{d(off)}$		52		
Fall time		t_f		19		

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS						
PARAMETER	CONDITIONS	SYMBOL	MIN	TYP.	MAX	UNIT
Drain forward voltage	$V_{GS} = 0V, I_F = 20A$	V_{SD}		0.9	1.2	V
Reverse recovery time	$V_R = 40V, I_F = 20A, di_F/dt = 100A/\mu s$	t_{rr}		51		ns
Reverse recovery charge		Q_{rr}		61		nC

TYPICAL DEVICE RATING AND CHARACTERISTICS CURVES ($T_A = 25^\circ\text{C}$ unless otherwise noted)

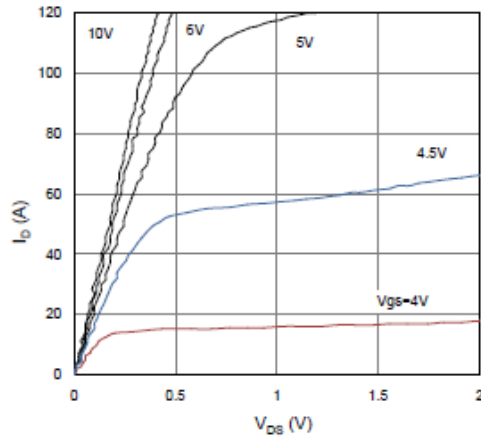


Fig.1 TYPICAL OUTPUT CHARACTERISTICS

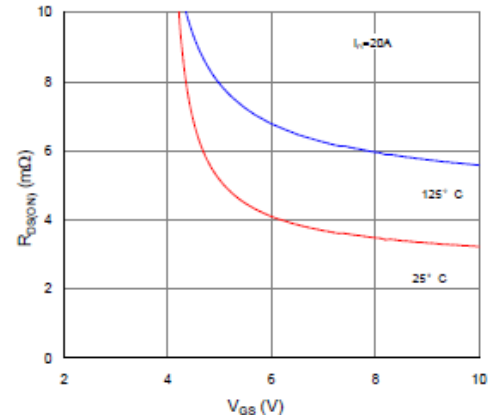


Fig.2 ON-RESISTANCE VS. GATE-SOURCE VOLTAGE

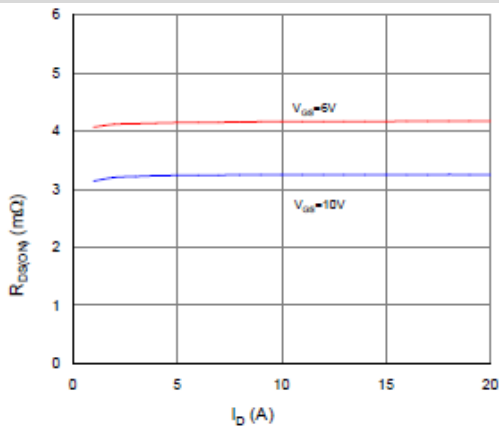


Fig.3 ON-RESISTANCE VS. DRAIN CURRENT AND GATE VOLTAGE

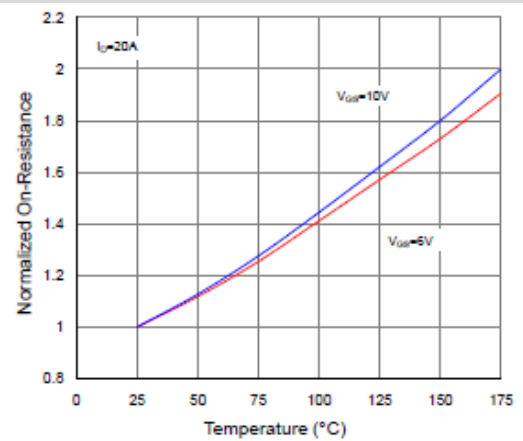


Fig.4 NORMALIZED ON-RESISTANCE VS. JUNCTION TEMPERATURE

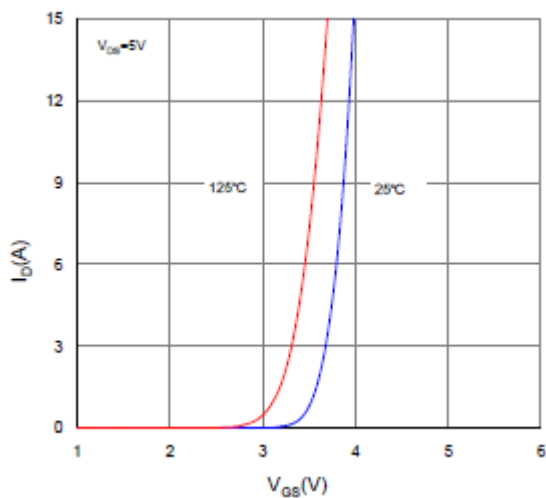


Fig.5 TYPICAL TRANSFER CHARACTERISTICS

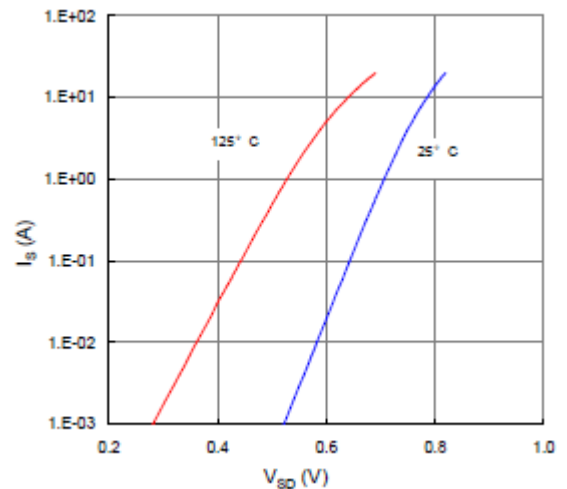


Fig.6 TYPICAL SOURCE-DRAIN DIODE FORWARD VOLTAGE

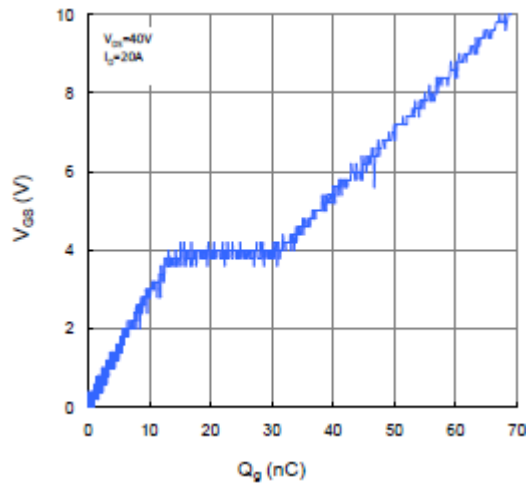


Fig.7 TYPICAL GATE-CHARGE VS. GATE-TO-SOURCE VOLTAGE

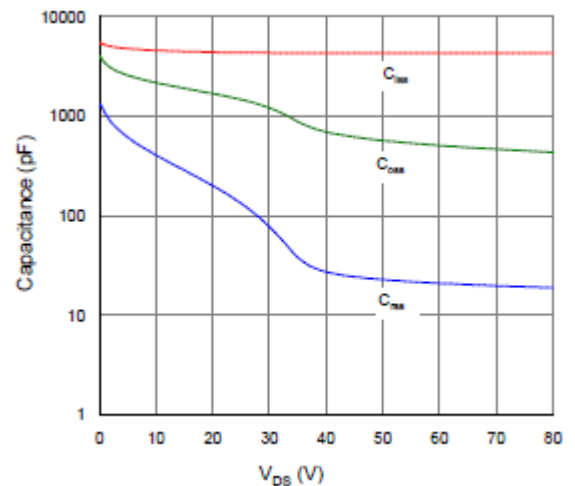


Fig.8 TYPICAL CAPACITANCE VS. DRAIN-TO-SOURCE VOLTAGE

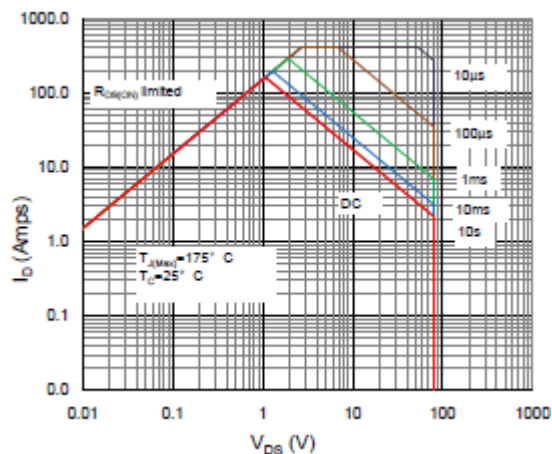


Fig.9 MAXIMUM SAFE OPERATING AREA

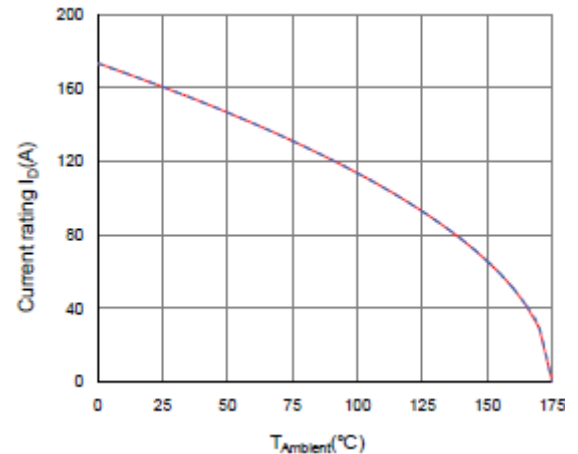


Fig.10 MAXIMUM DRAIN CURRENT VS. CASE TEMPERATURE

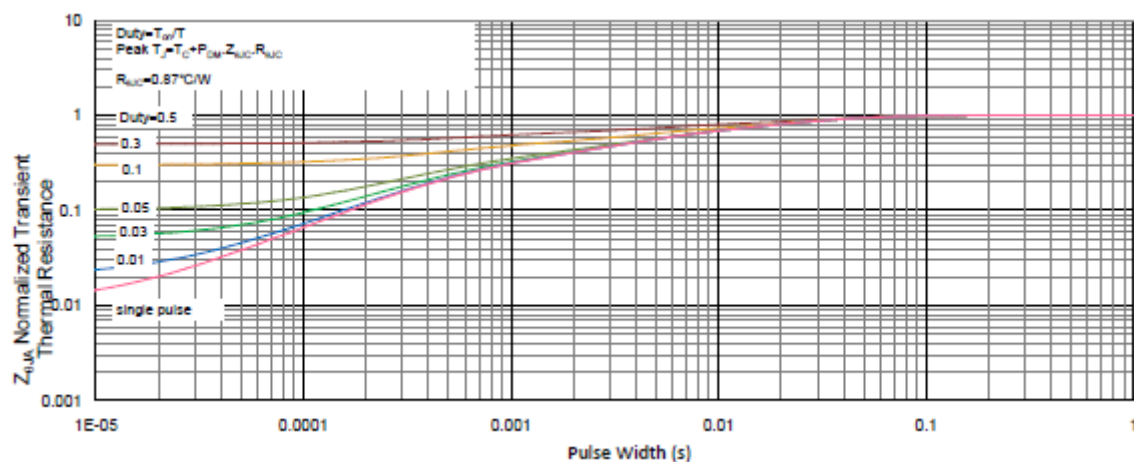
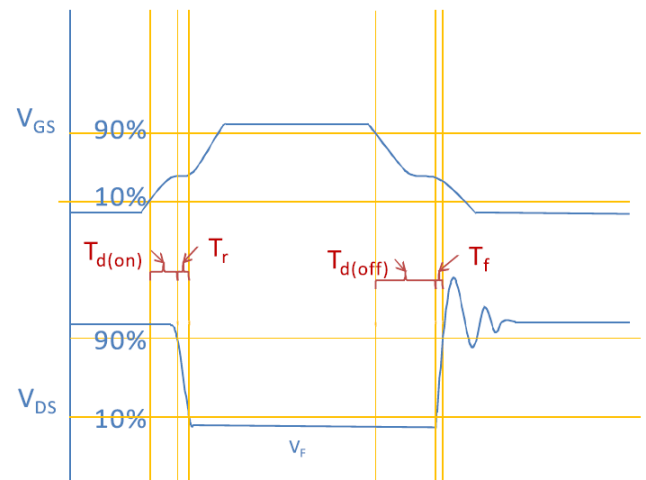
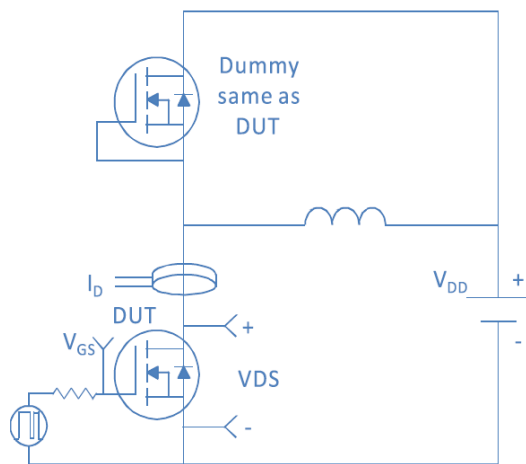
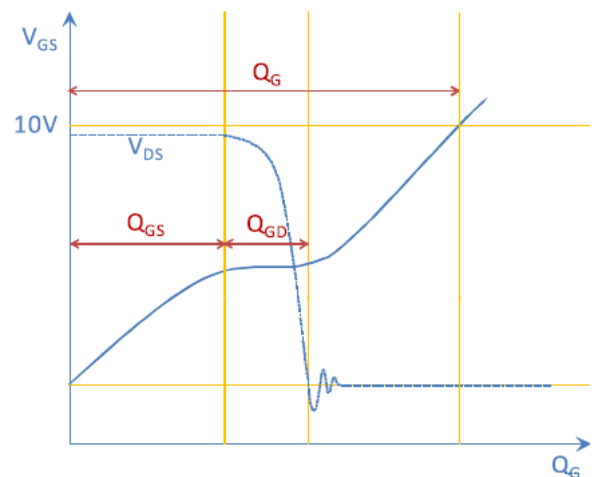
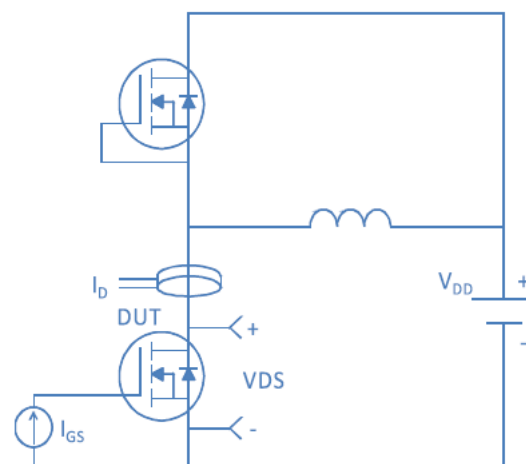


Fig.11 Normalized Maximum Transient Thermal Impedance, Junction-to-Ambient

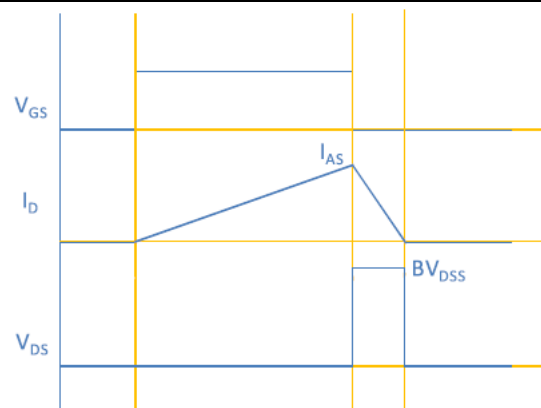
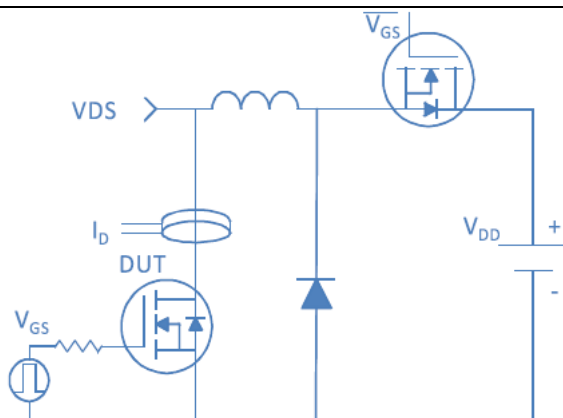
TEST RESULTS CURVES ($T_A = 25^\circ\text{C}$ unless otherwise noted)



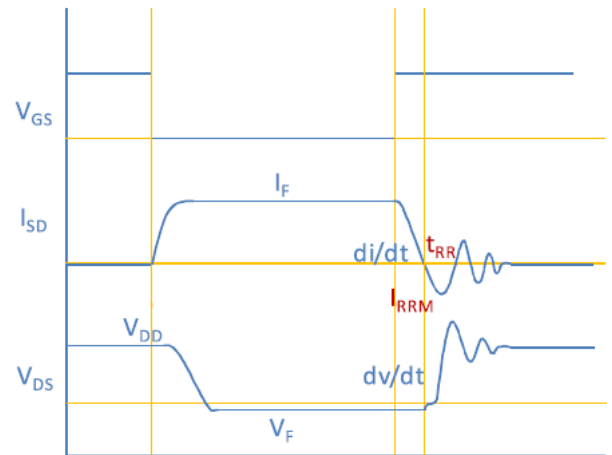
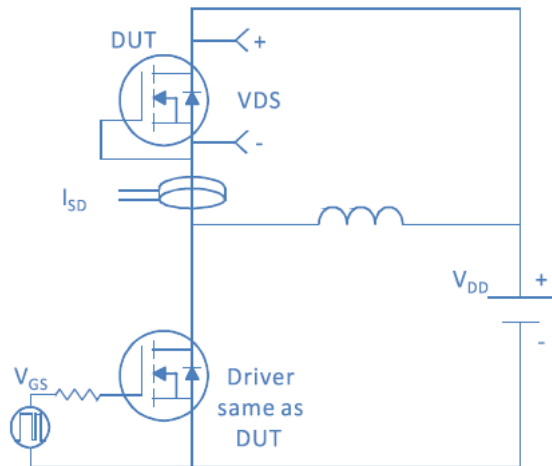
INDUCTIVE SWITCHING TEST



GATE CHARGE TEST



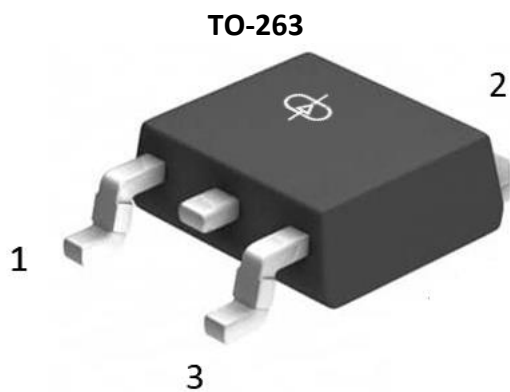
UCLAMPED INDUCTIVE SWITCHING (UIS) TEST



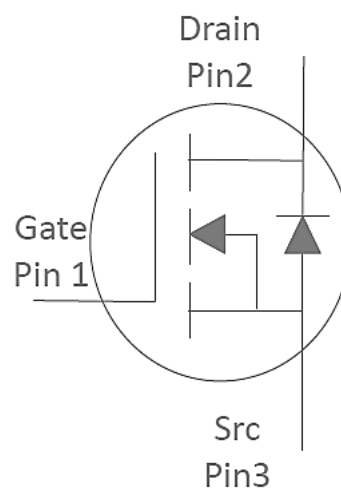
DIODE RECOVERY TEST

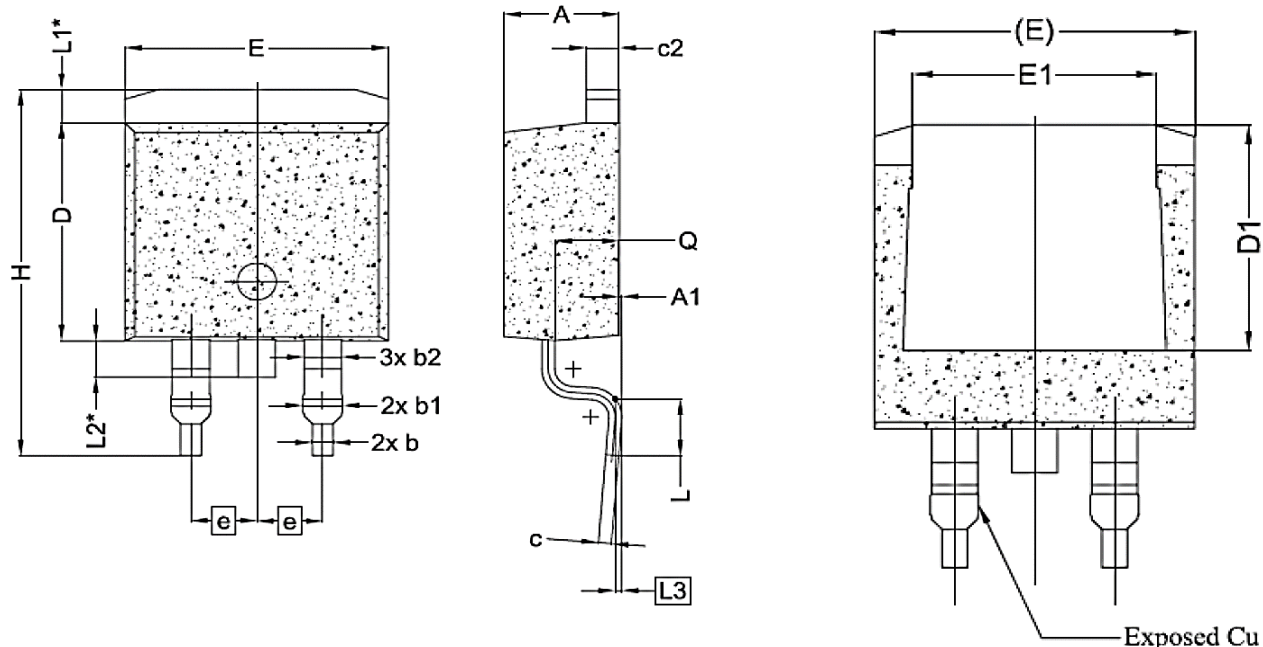
PINNING INFORMATION

SIMPLIFIED OUTLINE



SYMBOL



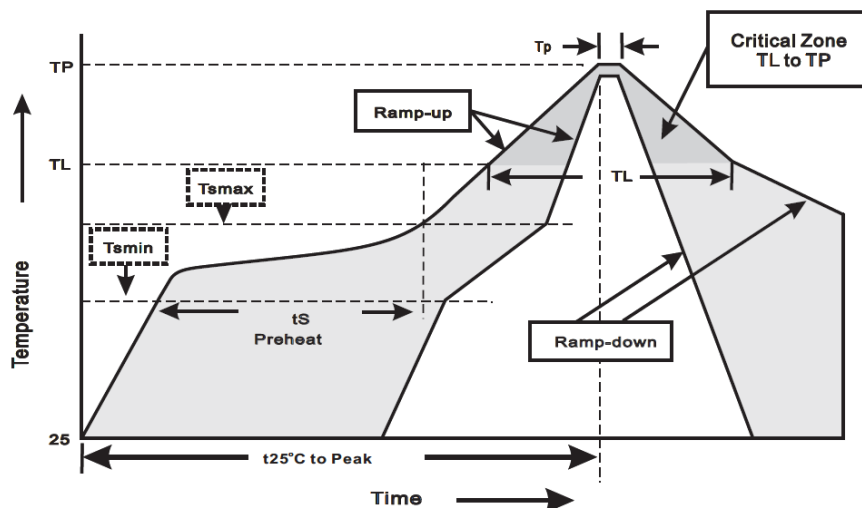
PACKAGE INFORMATION
TO-263, 3 Leads


OUTLINE DIMENSIONS						
SYMBOL	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	4.24	4.44	4.64	0.167	0.175	0.183
A1	0.00	0.10	0.25	0.000	0.004	0.010
b	0.70	0.80	0.90	0.028	0.031	0.035
b1	1.20	1.55	1.75	0.047	0.061	0.069
b2	1.20	1.45	1.70	0.047	0.057	0.067
c	0.40	0.50	0.60	0.016	0.020	0.024
c2	1.15	1.27	1.40	0.045	0.050	0.055
D	8.82	8.92	9.02	0.347	0.351	0.355
D1	6.86	7.65	-	0.270	0.301	-
E	9.96	10.16	10.36	0.392	0.400	0.408
E1	6.89	7.77	7.89	0.271	0.306	0.311
e	2.54 BSC			0.100 BSC		
H	14.61	15.00	15.88	0.575	0.591	0.625
L	1.78	2.32	2.79	0.070	0.091	0.110
L1	1.36 REF.			0.054 REF.		
L2	1.50 REF.			0.059 REF.		
L3	0.25 REF.			0.010 REF.		
Q	2.30	2.48	2.7	0.091	0.098	0.106

SOLDERING PARAMETERS

SUGGESTED THERMAL PROFILES FOR SOLDERING PROCESSES

1. Storage environment: Temperature=5 °C~40 °C Humidity=55% ±25%
2. Reflow soldering of surface-mount devices



3. Reflow soldering

PROFILE FEATURE	SOLDERING CONDITION
Average ramp-up rate (T_L to T_P)	<3 °C/sec
Preheat	
- Temperature Min (T_{smin})	150 °C
- Temperature Max (T_{smax})	200 °C
- Time (min to max) (t_s)	60 ~ 120 sec
T_{smax} to T_L	
- Ramp-upRate	<3 °C/sec
Time maintained above:	
- Temperature (T_L)	217 °C
- Time(t_L)	60 ~ 260 sec
Peak Temperature (T_P)	255 °C-0/+5 °C
Time within 5 °C of actual Peak Temperature(t_P)	10 ~ 30 sec
Ramp-down Rate	<3 °C/sec
Time 25 °C to Peak Temperature	<6 minutes



beyond boundaries...

ALPB035N08A

TO-263

CUSTOMER NOTE:

DISCLAIMER

The product information and the selection guide facilitates the selection of the ALPINESEMI™'s Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review the Data sheet(s) so as to confirm that the Device(s) meets functionality parameters for your application. The information furnished on the Data Sheet and the ALPINESEMI™'s Web Site is believed to be accurate and reliable at the time of preparation of this document. ALPINESEMI™ however, does not assume any inaccuracies that may arise when the components are mounted and removed. Furthermore, ALPINESEMI™ does not assume liability whatsoever, arising out of the application or the use of any of ALPINESEMI™'s product(s). Neither, does it convey any license under its patent rights nor the rights of others. These products are not guaranteed for use in life saving/support appliances or systems. ALPINESEMI™'s customers using these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and ALPINESEMI™ will not be responsible in any way(s) for any damage(s) resulting from such use.

Please check the website **www.alpinesemi.com** for continues updates and revision of datasheets.

DESIGN CHANGES: ALPINESEMI™ strives for continuous improvement and reserves the right to change the specifications of its products without prior notice. ALPINESEMI™ reserves the right to discontinue product lines without prior notice. Any product selection is a recommendation based on best understanding of such product(s) by our engineers. However, buyers are advised to rely on their own judgment for such selection of the products.

ALPINESEMI™ makes no warranty, representation or guarantee regarding the suitability of its products for any particular applications. Neither does ALPINESEMI™ assume any liability arising out of the applications nor the use of such products. ALPINESEMI™ specifically disclaims all liabilities either consequential or incidental.

All rights of the product and datasheet are reserved to ALPINESEMI™.

All logos and information provided in the datasheets are for reference only. Any registered and/or trademark/logos belonging to respective companies be the property of those companies. ALPINESEMI™ extends the courtesy to them, if any of the information found in its datasheet.

Component Disposal Instructions

1. ALPINESEMI™ Semiconductor Devices are RoHS compliant and hence customers are requested to dispose as per the prevailing Environmental Legislation put forth in their specific country.
2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).



sales@alpinesemi.com

www.alpinesemi.com