

beyond boundaries...

AXIAL LEADED TRANSIENT VOLTAGE SUPPRESSOR



5000W Axial Leaded Transient Voltage Suppressors – 5.0V ~ 220V

The 5KP Series is designed specifically to protect sensitive electronic equipment from voltage transients induced by lightning and other transient voltage events.

Bidirectional



Uni-directional



FEATURES:

- Axial lead type devices for through hole design.
- 5kW peak pulse capability with at 10/1000μs waveform, repetition rate (duty cycles): 0.01%.
- Excellent clamping capability.
- > Low incremental surge resistance.
- ➤ Fast response time from 0V to V_{BR}, typically less than 1 ps for uni-directional & 5 ns for bi-directional types.
- Glass passivated chip junction in P600 package.
- Lead-free parts meet RoHS requirements.
- Suffix "-H" indicates Halogen-free part, ex. ALP5KP17A-H.

APPLICATIONS:

TVS devices are ideal for the protection of I/O interfaces, VCC bus and other vulnerable circuits used in telecom, computer, industrial and consumer electronic applications.

MECHANICAL CHARACTERISTICS

- Epoxy: UL94-V0 rated flame retardant.
- Case: Molded plastic, P600.
- ➤ Lead: Axial leads, solderable per MIL-STD-202, method 208 guaranteed
- Polarity: Color band denotes cathode end except Bipolar
- Mounting Position: Any.
- Approximate Weight: 2.10 grams.

ALP5KPXXXA/CA SERIES

TYPICAL DEVICE CHARACTERISTICS

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	VALUE	UNIT			
Operating Junction Temperature range	TJ	-55 to +150	°C			
Storage Temperature range	T _{STG}	-55 to 150	°C			
Maximum instantaneous forward voltage at 100A for uni-directional types only, Note 3	VF	3.5/5.0	V			
Peak Pulse Power Dissipation by 10/1000µs Test Waveform (Fig.1) (Note 1)	P _{PPM}	15000	W			
Peak Pulse current with a 10/1000μs Waveform	Іррм	See Table 1	А			
Steady State Power Dissipation at T _L =75°C lead length 0.375" (9.5mm)	P _{M(AV)}	8.0	W			
Peak Forward Surge Current, 8.3ms Single Half Sine Wave (JEDEC Method) (Note 2)	I _{FSM}	400	А			

Notes:

- 1. Non-repetitive current pulse, per Fig. 3 and derated above T_J (initial) =25°C per Fig. 2.
- 2. Measured on 8.3ms single half sine wave or equivalent square wave, duty cycle=4 pulses per minute maximum.
- 3: $V_F \le 3.5V$ for devices of $V_{BR} \le 200V$ and $V_F \le 5.0V$ for devices of $V_{BR} \ge 201V$.



TYPICAL ELECTRICAL CHARACTERISTICS - (TABLE 1)

ELE	CTRICAL CHARAC	CTERISTICS I	PER LINE @ 1	T _A =25°C Unless C	Otherwise Specified		
PART NUMBER (Note 1)	REVERSE STAND- OFF VOLTAGE V _{RWM}	BREAKDOWN VOLTAGE VBR Br VOLTS		TEST CURRENT @ I _T mA	MAXIMUM CLAMPING VOLTAGE V _C @ I _{PP}		MAXIMUM REVERSE LEAKAGE I _R @V _{Rwm}
, ,	VOLTS	MIN	MAX	IIIA	VOLTS	I _{PP} (A)	(μΑ)
ALP5KP5.0A / CA	5.0	6.40	7.00	50	9.2	543	5000
ALP5KP6.0A / CA	6.0	6.67	7.37	50	10.3	485	5000
ALP5KP6.5A / CA	6.5	7.22	7.98	50	11.2	447	2000
ALP5KP7.0A / CA	7.0	7.78	8.60	50	12.0	417	1000
ALP5KP7.5A / CA	7.5	8.33	9.21	5.0	12.9	388	250
ALP5KP8.0A / CA	8.0	8.89	9.83	5.0	13.6	367	150
ALP5KP8.5A / CA	8.5	9.44	10.4	5.0	14.4	347	50
ALP5KP9.0A / CA	9.0	10.0	11.1	5.0	15.4	325	20
ALP5KP10A / CA	10.0	11.1	12.3	5.0	17.0	294	15
ALP5KP11A / CA	11.0	12.2	13.5	5.0	18.2	274	2
ALP5KP12A / CA	12.0	13.3	14.7	5.0	19.9	251	2
ALP5KP13A / CA	13.0	14.4	15.9	5.0	21.5	232	2
ALP5KP14A / CA	14.0	15.6	17.2	5.0	23.2	215	2
ALP5KP15A / CA	15.0	16.7	18.5	5.0	24.4	206	2
ALP5KP16A / CA	16.0	17.8	19.7	5.0	26.0	192	2
ALP5KP17A / CA	17.0	18.9	20.9	5.0	27.6	181	2
ALP5KP18A / CA	18.0	20.0	22.1	5.0	29.2	172	2
ALP5KP20A / CA	20.0	22.2	24.5	5.0	32.4	154	2
ALP5KP22A / CA	22.0	24.0	26.9	5.0	35.5	144	2
ALP5KP24A / CA	24.0	26.7	29.5	5.0	38.9	128	2
ALP5KP26A / CA	26.0	28.9	31.9	5.0	42.1	119	2
ALP5KP28A / CA	28.0	31.1	34.4	5.0	45.4	110	2
ALP5KP30A / CA	30.0	33.3	36.8	5.0	48.4	103	2
ALP5KP33A / CA	33.0	36.7	40.6	5.0	53.3	94	2
ALP5KP36A / CA	36.0	40.0	44.2	5.0	58.1	86	2
ALP5KP40A / CA	40.0	44.4	49.1	5.0	64.5	78	2
ALP5KP43A / CA	43.0	47.8	52.8	5.0	69.4	72	2
ALP5KP45A / CA	45.0	50.0	55.3	5.0	72.7	69	2
ALP5KP48A / CA	48.0	53.3	58.9	5.0	77.4	65	2
ALP5KP51A / CA	51.0	56.7	62.7	5.0	82.4	61	2
ALP5KP54A / CA	54.0	60.0	66.3	5.0	87.1	57	2
ALP5KP58A / CA	58.0	64.4	71.2	5.0	93.6	53	2
ALP5KP60A / CA	60.0	66.7	73.7	5.0	96.8	52	2
ALP5KP64A / CA	64.0	71.1	78.6	5.0	103.0	49	2
ALP5KP70A / CA	70.0	77.8	86.0	5.0	113.0	44	2



beyond boundaries...

ALP5KPXXXA/CA SERIES

ALP5KP75A / CA	75.0	83.3	92.1	5.0	121.0	41	2
ALP5KP78A / CA	78.0	86.7	95.8	5.0	126.0	40	2
ALP5KP85A / CA	85.0	94.4	104.0	5.0	137.0	36	2
ALP5KP90A / CA	90.0	100.0	111.0	5.0	146.0	34	2
ALP5KP100A / CA	100.0	110.0	123.0	5.0	162.0	31	2
ALP5KP110A / CA	110.0	122.0	135.0	5.0	177.0	28	2
ALP5KP120A / CA	120.0	133.0	147.0	5.0	193.0	26	2
ALP5KP130A / CA	130.0	144.0	159.0	5.0	209.0	24	2
ALP5KP150A / CA	150.0	167.0	185.0	5.0	243.0	21	2
ALP5KP160A / CA	160.0	178.0	197.0	5.0	259.0	19	2
ALP5KP170A / CA	170.0	189.0	209.0	5.0	275.0	18	2
ALP5KP180A / CA	180.0	200.0	220.0	5.0	292.0	17	2
ALP5KP190A / CA	190.0	211.0	232.0	5.0	310.0	16	2
ALP5KP200A / CA	200.0	224.0	247.0	5.0	329.0	15	2
ALP5KP210A / CA	210.0	233.0	258.0	5.0	350.0	15	2
ALP5KP220A / CA	220.0	246.0	272.0	5.0	371.0	14	2

NOTE

- 1. V_{BR} measured after I_T applied for 300us, I_T =square wave pulse or equivalent.
- Surge current waveform per Fig. 3 and derated per Fig. 2 2.
- Suffix 'C' denotes bi-directional devices. Suffix 'A' denotes 5% tolerance devices, no suffix denotes 10% tolerance devices. 3.
- 4. Part numbers with "CA" suffix are bidirectional devices, i.e., ALP15KPA17CA
- For bidirectional type having $V_{\text{\scriptsize Rwm}}$ of 10 volts and less, the I_R limit is double. 5.
- All term and symbols are consistent with ANS/IEEE C62.35

TYPICAL DEVICE CHARACTERISTICS

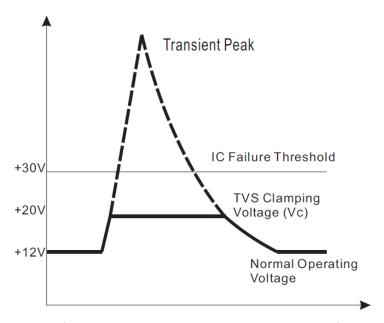


Fig. A – Transients of several thousand volts can be clamped to a safe level by the TVS

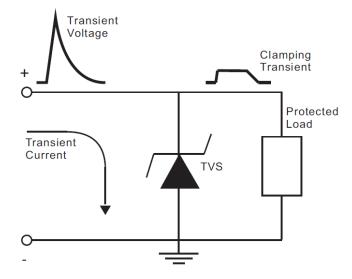


Fig. B – Transient current is diverted to ground thru TVS; the voltage seen by the protected load is limited to the clamping voltage level

beyond boundaries...

RATING AND CHARACTERISTIC CURVES

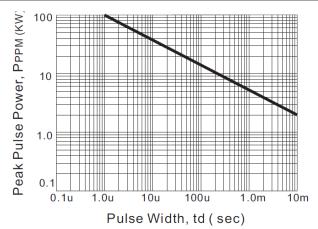


Fig1. PEAK PULSE POWER RATING CURVE

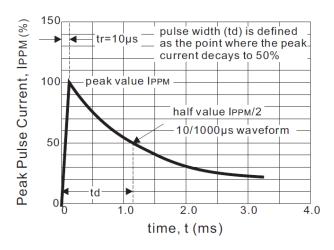


Fig3. PULSE WAVEFORM

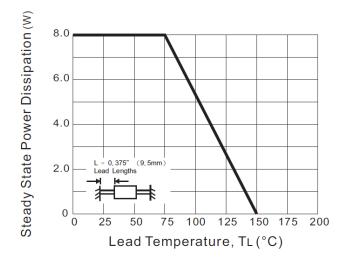


Fig5. STEADY STATE POWER DERATING CURVE

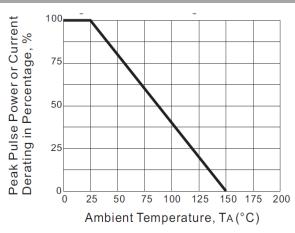
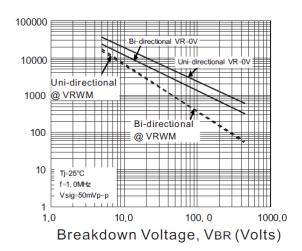


Fig2. PULSE DERATING CURVE



Junction Capacitance, CJ (pF)

Fig4. TYPICAL JUNCTION CAPACITANCE

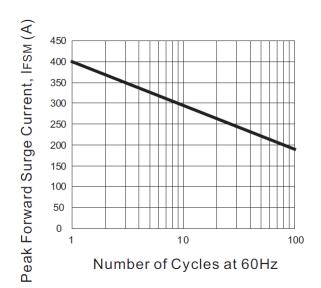
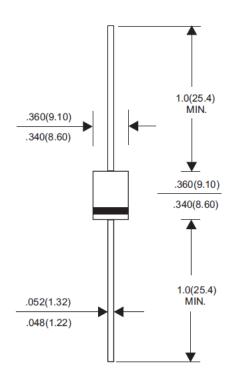


Fig6. MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

PACKAGE INFORMATION

PACKAGE OUTLINE

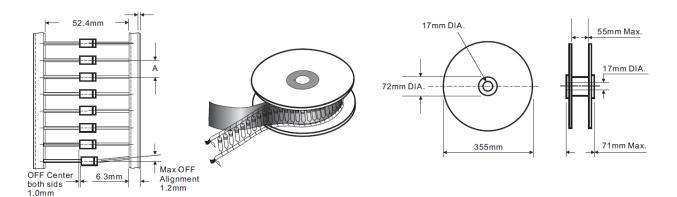


Dimensions in inches and (millimeters)

PINNING INFORMATION

I IIIIIIII OIIIIAI		
PIN	SIMPLIFIED OUTLINE	SYMBOL
Uni-Directional Pin1 Cathode Pin2 Anode	1 2	12
Bi-Directional		———

TAPING & BULK SPECIFICATIONS FOR AXIAL DEVICES



REEL PACKING

DEVICE CASE	QTY 1	COMPONENT	CARTON	QTY 2	APROX.
TYPE	(PCS / REEL)	SPCING	SIZE	(PCS / CARTON)	CROSS
		"A" in FIG. A	(m/m)		WEIGHT (kg)
P600	1000	10 mm	360 * 340 * 370	4000	9.5

AMMO PACKING

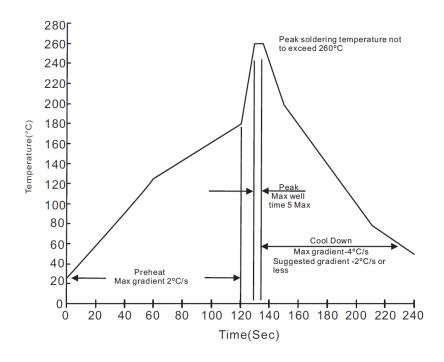
DEVICE CASE	QTY 1	INNER	CARTON	QTY 2	APROX.
TYPE	(PCS / BOX)	BOX SIZE	SIZE	(PCS / CARTON)	CROSS
		(m/m)	(m/m)		WEIGHT (kg)
P600	400	255 * 75 * 150	339 * 276 * 330	3200	9.2

BULK PACKING

DEVICE CASE TYPE	QTY 1 (PCS / BOX)	INNER BOX SIZE (m/m)	CARTON SIZE (m/m)	QTY 2 (PCS / CARTON)	APROX. CROSS WEIGHT (kg)
P600	250	194 * 84 * 20	355 * 320 * 280	6000	14.5

SOLDERING PARAMETERS

Lead Free temperature Profile wave-soldering



PRODUCT HIGH RELIABLITY TEST CAPABILITIES

ITEM	TEST CONDITIONS	STANDARD
Solder Resistance	At 260±5°C for 10±Sec,	MIL-STD-750D
	Immerse body into solder 1/16"±1/32"	METHOD-2031
Solderability	At 245±5°C for 5 sec.	MIL-STD-202F
		METHOD-208
Pull Test	2.0kg in axial lead direction for 10 sec.	MIL-STD-750D
		METHOD-2036
Bend Lead	2.0kg weight applied to each lead bending	MIL-STD-750D
	arc 90°±5° for 3 times	METHOD-2036
High Temperature Reverse Bias	V _{BR} = V _{BR} N _{OM} *80% at T _J =150° for 168 hrs.	MIL-STD-750D
		METHOD-1038
Pressure Cooker	15P _{SIG} at T _A =121°C for 4Hrs	JESD22-A102
Temperature Cycling	-55°C to +125°C dwelled for 30min and	MIL-STD-750D
	transferred for 5min. total 10 cycles.	METHOD-1051
Humidity	At T _A =85°C, RH=85% for 1000hrs.	MIL-STD-750D
		METHOD-1021
High Temperature Storage Life	At 175°C for 1000hrs.	MIL-STD-750D
		METHOD-1031



CUSTOMER NOTE:

DISCLAIMER

The product information and the selection guide facilitates the selection of the ALPINESEMI™'s Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review the Data sheet(s) so as to confirm that the Device(s) meets functionality parameters for your application. The information furnished on the Data Sheet and the ALPINESEMI™'s Web Site is believed to be accurate and reliable at the time of preparation of this document. ALPINESEMI™ however, does not assume any inaccuracies that may arise when the components are mounted and removed. Furthermore, ALPINESEMI™ does not assume liability whatsoever, arising out of the application or the use of any of ALPINESEMI™'s product(s). Neither, does it convey any license under its patent rights nor the rights of others. These products are not guaranteed for use in life saving/support appliances or systems. ALPINESEMI™'s customers using these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and ALPINESEMI™ will not be responsible in any way(s) for any damage(s) resulting from such use.

Please check the website www.alpinesemi.com for continues updates and revision of datasheets.

DESIGN CHANGES: ALPINESEMI™ strives for continuous improvement and reserves the right to change the specifications of its products without prior notice. ALPINESEMI™ reserves the right to discontinue product lines without prior notice. Any product selection is a recommendation based on best understanding of such product(s) by our engineers. However, buyers are advised to rely on their own judgment for such selection of the products.

ALPINESEMI™ makes no warranty, representation or guarantee regarding the suitability of its products for any particular applications. Neither does ALPINESEMI™ assume any liability arising out of the applications nor the use of such products. ALPINESEMI™ specifically disclaims all liabilities either consequential or incidental.

All rights of the product and datasheet are reserved to ALPINESEMI™.

All logos and information provided in the datasheets are for reference only. Any registered and/or trademark/logos belonging to respective companies be the property of those companies. ALPINESEMI™ extends the courtesy to them, if any of the information found in its datasheet.

Component Disposal Instructions

- 1. ALPINESEMI™ Semiconductor Devices are RoHS compliant and hence customers are requested to dispose as per the prevailing Environmental Legislation put forth in their specific country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).



sales@alpinesemi.com www.alpinesemi.com