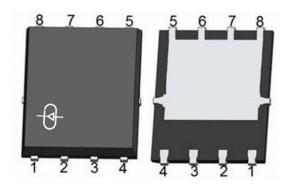


110A, 100V N-CHANNEL SUPER TRENCH POWER MOSFET

DESCRIPTION:



The ALP110N10S is an 110A, 100V N-Channel Super Trench Power MOSFET and it has fast a High density cell design for low $R_{\text{DS(on)}}$

FEATURES:

- $V_{(BR)DSS} = 100V, I_D = 110A$
- $Arr R_{DS(ON)MAX} = 5.0 mΩ$ @V_{GS} = 10V, I_D = 110A.
- $Arr R_{DS(ON)MAX} = 6.5 mΩ$ @V_{GS} = 4.5V, I_D = 110A.
- Split gate trench MOSFET technology
- High density cell design for low R_{DS(on)}.
- > Excellent package for heat dissipation.
- Lead-free parts meet RoHS requirements
- Suffix "-H" indicated Halogen Free part, ex. ALP110N10S-H

MECHANICAL CHARACTERISTICS

- Case: Molded plastic, DFN5X6-8L
- Mounting Position: Any.

APPLICATIONS:

- DC to DC Converter.
- Power switching application.
- Uninterruptible power supply.



MAXIMUM RATINGS

MAXIMUM RATINGS @ T_A = 25 °C unless otherwise specified					
PARAMETER	SYMBOL	RATINGS	UNIT		
Drain-Source Voltage	V_{DS}	100	V		
Gate-Source Voltage	V_{GS}	±20	V		
Continuous Drain Current					
T _C =25°C	I _D	110	Α		
T _C =100°C		70			
Pulsed Drain Current (Note 1)	I _{DM}	440	А		
Power Dissipation T _C =25°C (Note 2)	P_D	113	W		
Thermal Resistance Junction to Ambient (Note 4)	$R_{\theta JA}$	53	°C/W		
Thermal Resistance Junction to Case	$R_{ heta JC}$	1.1	°C/W		
Single pulse avalanche energy (Note 3)	E _{AS}	400	mJ		
Operating Junction Temperature	TJ	+150	°C		
Storage Temperature Range	T _{STG}	-55 to +150	°C		

Note:

- 1. Repetitive rating; pulse width limited by max. junction temperature.
- 2. Pd is based on max. junction temperature, using junction-case thermal resistance.
- 3. EAS condition: T_j =25°C, V_{DD} =50V, V_{GS} =10V, R_G =2.5 Ω , L=2mH I_{as} =20A
- 4. The value of $R_{\theta JA}$ is measured with the device mounted on 1in2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C.



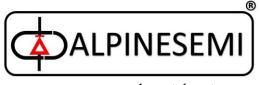
ELECTRICAL CHARACTERISTICS @ TA = 25 °C unless otherwise specified

PARAMETER	CONDITIONS	SYMBOL	MIN	TYP.	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250 μA	V _{(BR)DSS}	100			٧
Zero gate voltage drain current	V _{DS} =100V, V _{GS} =0V	I _{DSS}			1.0	μΑ
Gate-body leakage current	V _{GS} = ±20V, V _{DS} = 0V	Igss			±100	nA
ON CHARACTERISTICS						
Gate-Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	$V_{GS(th)}$	1.0	1.7	2.5	V
Drain-to-Source On-Resistance	$V_{GS} = 10V, I_D = 20A$			3.8	5.0	
Diani-to-source Off-Resistance	$V_{GS} = 4.5V$, $I_D = 20A$	R _{DS(ON)}		4.8	6.5	m $Ω$

DYNAMIC CHARACTERISTICS (Note 2)						
PARAMETER	CONDITIONS	SYMBOL	MIN	TYP.	MAX	UNIT
Input Capacitance	$V_{DS} = 50V$, $V_{GS} = 0V$, $F_{req} = 1.0$ MHz	C _{iss}		4300		pF
Output Capacitance	$V_{DS} = 50V$, $V_{GS} = 0V$, $F_{req} = 1.0$ MHz	Coss		1800		pF
Reserve Transfer Capacitance	V _{DS} = 50V, V _{GS} = 0V, F _{req} = 1.0 MHz	Crss		30		pF

SWITCHING CHARACTERISTICS						
PARAMETER	CONDITIONS	SYMBOL	MIN	TYP.	MAX	UNIT
Total Gate Charge	V _{DS} = 50V, V _{GS} = 10V, I _D = 55A	Q_g		50		nC
Gate to Source Charge	$V_{DS} = 50V$, $V_{GS} = 10V$, $I_{D} = 55A$	Qgs		4.5		nC
Gate to Drain Charge	V _{DS} = 50V, V _{GS} = 10V, I _D = 55A	Q _{gd}		7		nC
Turn-On Delay Time	$V_{DS} = 50V$, $V_{GS} = 10V$, $I_{D} = 55A$, $R_{G} = 2.2\Omega$	t _{d(on)}		22		nS
Turn-On Rise time	$V_{DS} = 50V$, $V_{GS} = 10V$, $I_{D} = 55A$, $I_{r} = 55A$		110		nS	
Turn-Off Delay Time	$V_{DS} = 50V$, $V_{GS} = 10V$, $I_{D} = 55A$, $R_{G} = 2.2\Omega$	t _{d(off)} 38		nS		
Turn-Off Fall time	$V_{DS} = 50V$, $V_{GS} = 10V$, $I_{D} = 55A$, $R_{G} = 2.2\Omega$	t _f		8		nS

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS						
PARAMETER	CONDITIONS SYMBOL MIN TYP. MAX					
Diode forward Current		Is			110	Α
Diode forward Voltage	I _S = 55A, V _{GS} = 0V	V_{DS}			1.2	V
Reverse recovery time	$T_J = 25$ °C, $I_F = 55A$, $dI_F/dt = 100A/\mu s$	t _{rr}		48		nS
Reverse recovery charge	$T_J = 25^{\circ}C$, $I_F = 55A$, $dI_F/dt = 100A/\mu s$	Qrr		150		nC



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TYPICAL DEVICE RATING AND CHARACTERISTICS CURVES (TA = 25 °C unless otherwise noted)

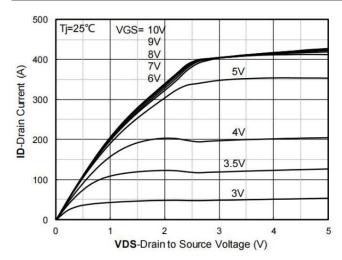


Fig.1 OUTPUT CHARACTERISTICS

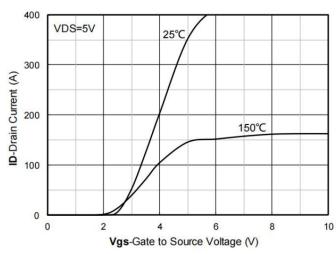


Fig.2 TRANSFER CHARACTERISTICS

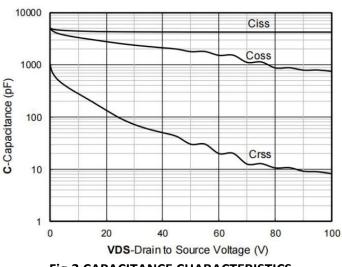


Fig.3 CAPACITANCE CHARACTERISTICS

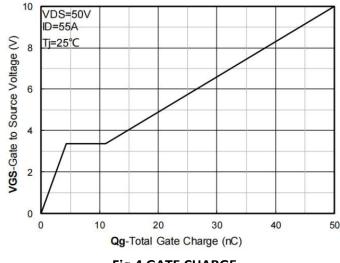


Fig.4 GATE CHARGE

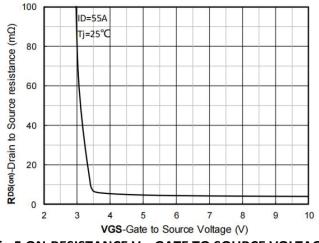


Fig.5 ON-RESISTANCE Vs. GATE TO SOURCE VOLTAGE

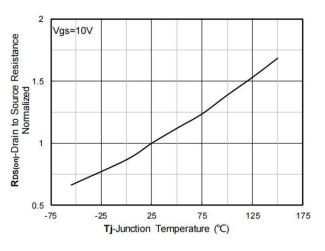


Fig. 6 NORMALIZED ON-RESISTANCE



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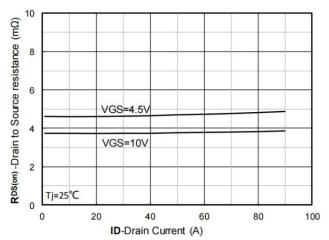
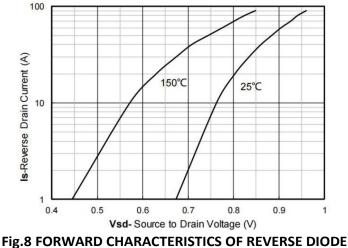


Fig.7 R_{DS(ON)} Vs. DRAIN CURRENT



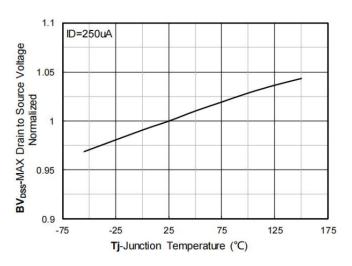


Fig.9 NORMALIZED BREAKDOWN VOLTAGE

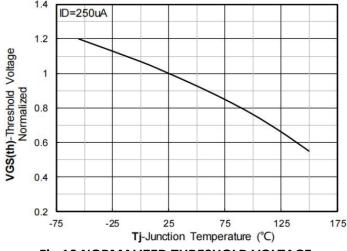


Fig. 10 NORMALIZED THRESHOLD VOLTAGE

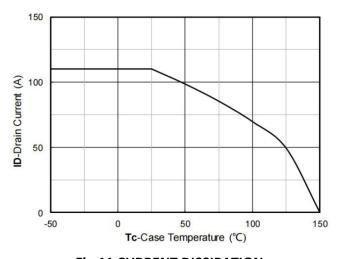


Fig.11 CURRENT DISSIPATION

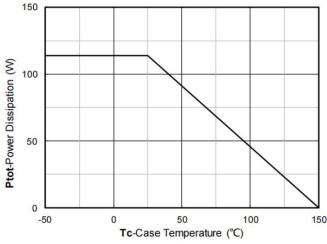
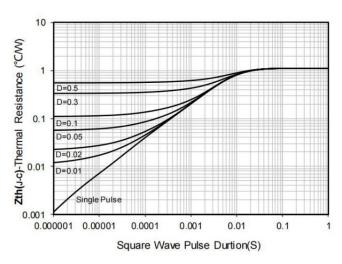


Fig.12 POWER DISSIPATION







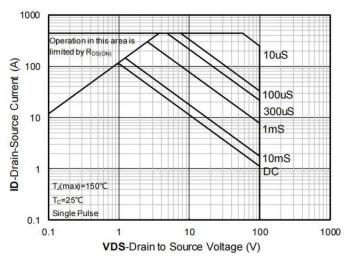


Fig.14 SAFE OPERATION AREA

PINNING INFORMATION

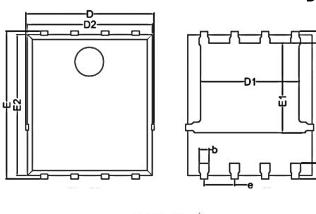
PIN	SIMPLIFIED OUTLINE		CIRCUIT DIAGRAM
Pin1 Source Pin2 Source Pin3 Source Pin4 Gate Pin5 Drain Pin6 Drain Pin7 Drain Pin8 Drain	1 2 3 4		

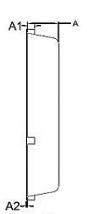


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PACKAGE INFORMATION

DFN5X6-8L





OUTLINE DIMENSIONS					
	MILLIMETERS		INCH	IES	
SYMBOL	MIN	MAX	MIN	MAX	
Α	1.000	1.200	0.039	0.047	
A1	0.25	4BSC.	0.010	BSC.	
A2	0.000	0.100	0.000	0.004	
b	0.310	0.510	0.012	0.020	
D	5.150	5.550	0.202	0.219	
D1	3.920	4.320	0.154	0.170	
D2	5.000	5.400	0.197	0.212	
E	5.950	6.350	0.234	0.250	
E1	3.520	3.920	0.139	0.154	
E2	5.660	6.060	0.223	0.239	
е	1270BSC.		0.050	BSC.	
L1	0.560	0.760	0.022	0.030	
L2	0.500BSC.		0.020	BSC.	

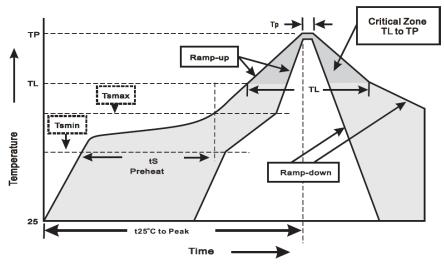


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SOLDERING PARAMETERS

SUGGESTED THERMAL PROFILES FOR SOLDERING PROCESSES

- 1. Storage environment: Temperature=5 °C~40 °C Humidity=55% ±25%
- 2. Reflow soldering of surface-mount devices



3. Reflow soldering

PROFILE FEATURE	SOLDERING CONDITION
Average ramp-up rate (T _L to T _P)	<3 °C/sec
Preheat	
- Temperature Min (T _{smin})	150 °C
- Temperature Max (T _{smax})	200 °C
- Time (min to max) (t _s)	60 ~ 120 sec
T _{smax} to T _L	
- Ramp-upRate	<3 °C/sec
Time maintained above:	
- Temperature (T _L)	217 °C
- Time(tL)	60 ~ 260 sec
Peak Temperature (T _P)	255 °C-0/+5 °C
Time within 5 °C of actual Peak	10 ~ 30 sec
Temperature(tP)	
Ramp-down Rate	<6 °C/sec
Time 25 °C to Peak Temperature	<6 minutes



CUSTOMER NOTE:

DISCLAIMER

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- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).



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