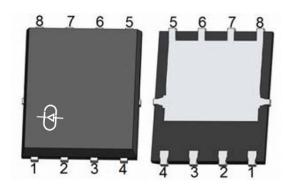


95A, 100V N-CHANNEL ENHANCEMENT MODE POWER MOSFET

DESCRIPTION:



The ALP095N10B is an 95A, 100V N-Channel Enhancement mode Power MOSFET and it has fast a High density cell design with low gate charge.

FEATURES:

- $V_{DS} = 100V, I_D = 95A$
- Arr R_{DS(ON)} = 6.0m Ω @V_{GS} = 10V, I_D = 95A.
- Low Gate Charge
- 100% E_{AS} guaranteed
- Lead-free parts meet RoHS requirements
- Suffix "-H" indicated Halogen Free part, ex. ALP095N10B-H

APPLICATIONS:

- DC to DC Converter.
- Power Management switches.
- > LED Backlighting.

MECHANICAL CHARACTERISTICS

Case: Molded plastic, PDFN5060-8L

Mounting Position: Any.



MAXIMUM RATINGS

MAXIMUM RATINGS @ T _A = 25 °C unless otherwise specified					
PARAMETER	SYMBOL	RATINGS	UNIT		
Drain-Source Voltage	V _{DS}	100	V		
Gate-Source Voltage	V _{GS}	±20	V		
Continuous Drain Current (Note 1) $T_{C} = 25^{\circ}C$ $T_{C} = 100^{\circ}C$	I _D	95 60	А		
Pulsed Drain Current (Note 4)	I _{DM}	380	А		
Single pulse avalanche energy (Note 3)	E _{AS}	245	mJ		
Total Power Dissipation T _C =25°C	P _D	113.6	w		
Thermal Resistance Junction to Ambient (Note 1)	$R_{ heta JA}$	60	°C/W		
Thermal Resistance Junction to Case	$R_{ heta JC}$	1.1	°C/W		
Operating Junction Temperature	Tı	-55 to +150	°C		
Storage Temperature Range	T _{STG}	-55 to +150	°C		



ELECTRICAL CHARACTERISTICS @ TA = 25 °C unless otherwise specified

PARAMETER		CONDITIONS	SYMBOL	MIN	TYP.	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakd Voltage	own	V _{GS} =0V, I _D =250 μA	V _{(BR)DSS}	100			V
Zero gate voltage	T _J =25°C	V 400V V 0V				1.0	
drain current	T _J =100°C	V _{DS} =100V, V _{GS} =0V	I _{DSS}			100	μΑ
Gate-body leakage current		V_{GS} = ±20V, V_{DS} = 0V	I _{GSS}			±100	nA
ON CHARACTERIST	ON CHARACTERISTICS						
Gate-Threshold Voltage		$V_{DS} = V_{GS}$, $I_{D} = 250 \mu A$	$V_{GS(th)}$	2.0	3.0	4.0	V
Drain-to-Source On-Resistance (Note 2)		$V_{GS} = 10V, I_D = 20A$	R _{DS(ON)}		4.6	6.0	mΩ
Forward Transconductance (Note 2)		$V_{DS} = 10V$, $I_D = 20A$	g fs		58		S

DYNAMIC CHARACTERISTICS						
PARAMETER	CONDITIONS	SYMBOL	MIN	TYP.	MAX	UNIT
Input Capacitance	$V_{DS} = 50V$, $V_{GS} = 0V$, $F_{req} = 1.0 \text{ MHz}$	C _{iss}		5518		pF
Output Capacitance	$V_{DS} = 50V$, $V_{GS} = 0V$, $F_{req} = 1.0 \text{ MHz}$	C _{oss}		655		pF
Reserve Transfer Capacitance	$V_{DS} = 50V$, $V_{GS} = 0V$, $F_{req} = 1.0$ MHz	C_{rss}		23		pF

SWITCHING CHARACTERISTICS						
PARAMETER	CONDITIONS	SYMBOL	MIN	TYP.	MAX	UNIT
Gate Resistance	$V_{DS} = 0V$, $V_{GS} = 0V$, $F_{req} = 1.0$ MHz	Rg		1.4		Ω
Total Gate Charge	$V_{DS} = 50V$, $V_{GS} = 10V$, $I_{D} = 20A$	Q_{g}		81.8		nC
Gate to Source Charge	$V_{DS} = 50V$, $V_{GS} = 10V$, $I_{D} = 20A$	Q_{gs}		23.5		nC
Gate to Drain Charge	$V_{DS} = 50V$, $V_{GS} = 10V$, $I_{D} = 20A$	Q_{gd}		22.5		nC
Turn-On Delay Time	$V_{DS} = 50V$, $V_{GS} = 10V$, $I_{D} = 20A$, $R_{G} = 3\Omega$	t _{d(on)}		15.4		nS
Turn-On Rise time	$V_{DS} = 50V$, $V_{GS} = 10V$, $I_{D} = 20A$, $R_{G} = 3\Omega$	t _r		13		nS
Turn-Off Delay Time	$V_{DS} = 50V$, $V_{GS} = 10V$, $I_{D} = 20A$, $R_{G} = 3\Omega$	t _{d(off)}		34		nS
Turn-Off Fall time	$V_{DS} = 50V$, $V_{GS} = 10V$, $I_{D} = 20A$, $R_{G} = 3\Omega$	t _f		6.2		nS

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS						
PARAMETER	CONDITIONS	SYMBOL	MIN	TYP.	MAX	UNIT
Continuous Source Current (Note 1, 5)	V _G =V _D =0V, Force Current	I _S			95	Α
Diode forward Voltage (Note 2)	$I_F = 20A$, $V_{GS} = 0V$	V_{DS}			1.2	V
Body Diode Reverse recovery time	T _J = 25°C, I _F = 20A, dI _F /dt =100A/μs	t _{rr}		55		nS
Body Diode Reverse recovery charge	T _J = 25°C, I _F = 20A, dI _F /dt =100A/μs	Q _{rr}		101		nC

Note:

- 1. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.
- 2. The data tested by pulsed, pulse width \leq 300 μ s, duty cycle \leq 2%
- 3. The E_{AS} data shows Max. rating. The test condition is V_{DD} =50V, V_{GS} =10V, L=0.4mH, I_{AS} =35A
- 4. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C.
- 5. The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.



TYPICAL DEVICE RATING AND CHARACTERISTICS CURVES (TA = 25 °C unless otherwise noted)

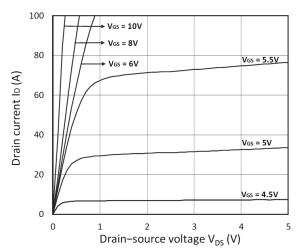


Fig.1 OUTPUT CHARACTERISTICS

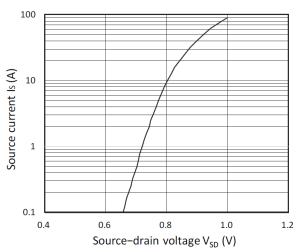


Fig.3 FORWARD CHARACTERISTICS OF REVERSE

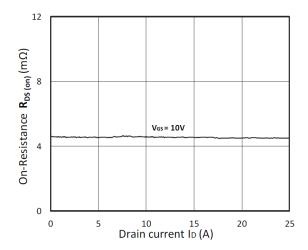


Fig.5 R_{DS(ON)} vs. I_D

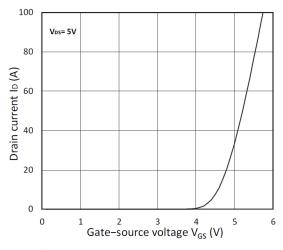


Fig.2 TRANSFER CHARACTERISTICS

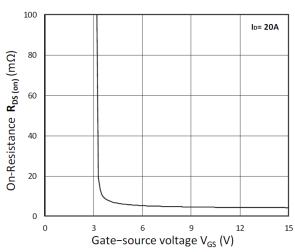


Fig.4 R_{DS(ON)} vs. V_{GS}

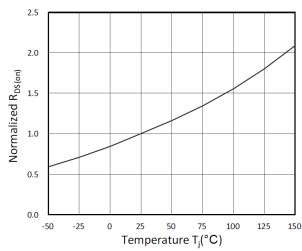


Fig. 6 NORMALIZED RDS(ON) vs. TEMPERATURE



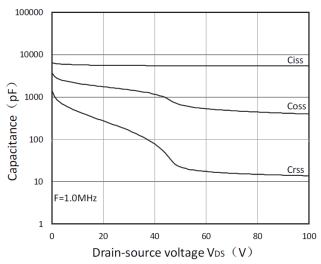


Fig.7 CAPACITANCE CHARACTERISTICS

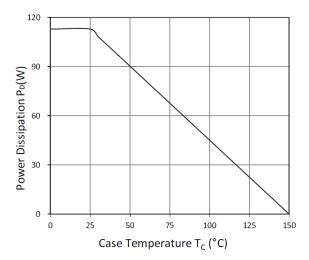


Fig.9 POWER DISSIPATION

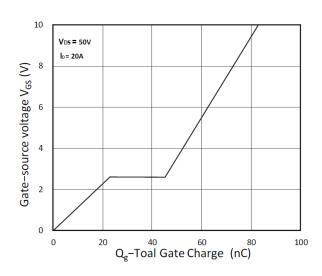


Fig.8 GATE CHARGE CHARACTERISTICS

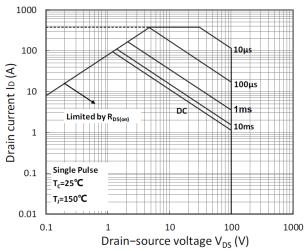


Fig.10 SAFE OPERATING AREA

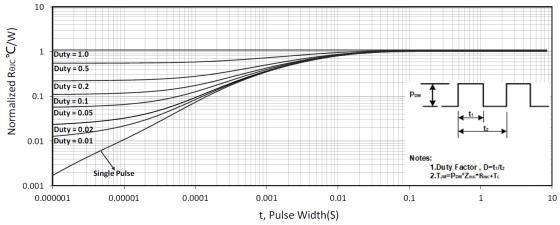


Fig.11 NORMALIZED MAXIMUM TRANSIENT THERMAL IMPEDANCE

TEST CIRCUIT

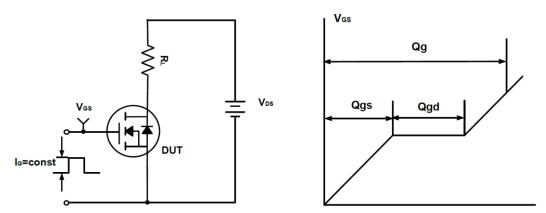


Figure A. GATE CHARGE TEST CIRCUIT & WAVEFORMS

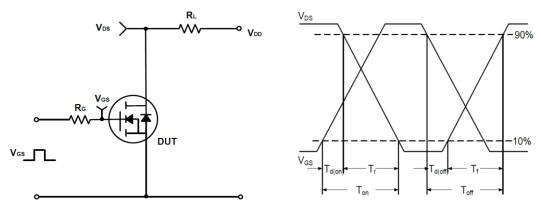


Figure B. SWITCHING TEST CIRCUIT & WAVEFORMS

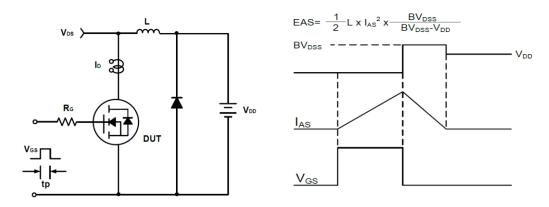


Figure C. UNCLAMPED INDUCTIVE SWITCHING CIRCUIT & WAVEFORMS



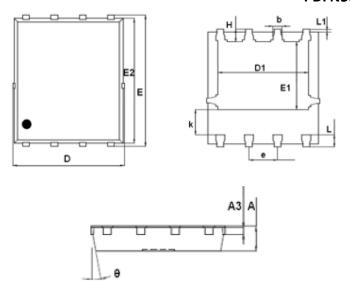
PINNING INFORMATION

PIN	SIMPLIFIED OUTLINE		CIRCUIT DIAGRAM
Pin1 Source Pin2 Source Pin3 Source Pin4 Gate Pin5 Drain Pin6 Drain Pin7 Drain Pin8 Drain	1 2 3 4		G G G



PACKAGE INFORMATION

PDFN5060-8L



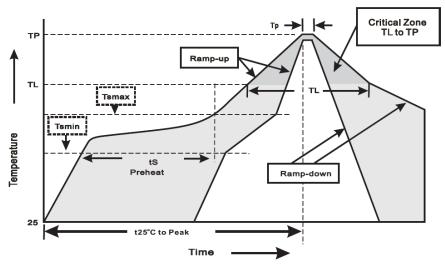
OUTLINE DIMENSIONS					
	MILLIMETERS				
SYMBOL	MIN	MAX			
А	0.90	1.20			
А3	0.15	0.35			
D	4.80	5.40			
E	5.90	6.35			
D1	3.61	4.31			
E1	3.30	3.92			
E2	5.50	6.06			
k	1.10	-			
b	0.30	0.51			
е	1	27BSC			
L	0.38	0.71			
L1	0.05	0.36			
Н	0.38	0.71			
θ	0°	12°			



SOLDERING PARAMETERS

SUGGESTED THERMAL PROFILES FOR SOLDERING PROCESSES

- 1. Storage environment: Temperature=5 °C~40 °C Humidity=55% ±25%
- 2. Reflow soldering of surface-mount devices



3. Reflow soldering

PROFILE FEATURE	SOLDERING CONDITION
Average ramp-up rate (T _L to T _P)	<3 °C/sec
Preheat	
- Temperature Min (T _{smin})	150 °C
- Temperature Max (T _{smax})	200 °C
- Time (min to max) (t _s)	60 ~ 120 sec
T _{smax} to T _L	
- Ramp-upRate	<3 °C/sec
Time maintained above:	
- Temperature (T _L)	217 °C
- Time(tL)	60 ~ 260 sec
Peak Temperature (T _P)	255 °C-0/+5 °C
Time within 5 °C of actual Peak	10 ~ 30 sec
Temperature(tP)	
Ramp-down Rate	<6 °C/sec
Time 25 °C to Peak Temperature	<6 minutes



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- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).



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