

4.8A, 20V Dual N-CHANNEL MOSFET

DESCRIPTION:



The ALP048N02I is a 4.8A, 20V Dual N-Channel MOSFET and it has fast switching speed with low on-resistance.

FEATURES: APPLICATIONS:

 $V_{DS} = 20V$, $I_D = 4.8A$

 $R_{DS(ON)} < 30 m\Omega @V_{GS} = 4.5V$

 $R_{DS(ON)} < 40 m\Omega @V_{GS} = 2.5V$

- Trench Power MOSFET
- Fast Switching Speed
- Low On-Resistance
- Green Device Available
- **RoHS Complaint**
- Suffix "-H" indicated Halogen Free part, ex. ALP048N02I-H

MECHANICAL CHARACTERISTICS

Case: Molded plastic, SOP-8L

Mounting Position: Any.

> Switching.





MAXIMUM RATINGS

MAXIMUM RATINGS @ T _A = 25 °C unless otherwise specified							
PARAMETER SYMBOL RATINGS UNIT							
Drain-Source Voltage	V_{DS}	20	V				
Gate-Source Voltage	V_{GS}	±12	V				
Continuous Drain Current @T _C =25°C	I _D	4.8	А				
Pulsed Drain Current (Note 2)	I _{DM}	30	А				
Total Power Dissipation T _C =25°C	P _D	1.25	W				
Thermal Resistance Junction to Ambient (Note 1)	$R_{ heta JA}$	100	°C/W				
Operating Junction Temperature	Tı	-55 to +150	°C				
Storage Temperature Range	T _{STG}	-55 to +150	°C				



ELECTRICAL CHARACTERISTICS @ TA = 25 °C unless otherwise specified

PARAMETER	PARAMETER CONDITIONS		MIN	TYP.	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250 μA	V _{(BR)DSS}	20			٧
Zero gate voltage drain current V _{DS} =20V, V _{GS} =0V		I _{DSS}			1.0	μΑ
Gate-body leakage current	V _{GS} = ±12V, V _{DS} = 0V	I _{GSS}			±100	nA
ON CHARACTERISTICS						
Gate-Threshold Voltage (Note 3)	$V_{DS} = V_{GS}$, $I_{D} = 250 \mu A$	V _{GS(th)}	0.5	0.7	1.2	V
Drain-to-Source On-Resistance	$V_{GS} = 4.5V$, $I_D = 4.8A$			20	30	O
(Note 3)	$V_{DS} = 2.5V, I_D = 4A$	R _{DS(ON)}		25	40	mΩ

DYNAMIC CHARACTERISTICS						
PARAMETER	CONDITIONS	SYMBOL	MIN	TYP.	MAX	UNIT
Input Capacitance	$V_{DS} = 10V$, $V_{GS} = 0V$, $F_{req} = 1.0$ MHz	C _{iss}		515		pF
Output Capacitance	$V_{DS} = 10V$, $V_{GS} = 0V$, $F_{req} = 1.0$ MHz	C _{oss}		73		pF
Reserve Transfer Capacitance	$V_{DS} = 10V$, $V_{GS} = 0V$, $F_{req} = 1.0$ MHz	C _{rss}		65		pF

SWITCHING CHARACTERISTICS (Note 4)						
PARAMETER	CONDITIONS	SYMBOL	MIN	TYP.	MAX	UNIT
Total Gate Charge	$V_{DS} = 10V$, $V_{GS} = 4.5V$, $I_D = 3A$	Q_g		10		nC
Gate to Source Charge	$V_{DS} = 10V$, $V_{GS} = 4.5V$, $I_D = 3A$	Q_{gs}		1.5		nC
Gate to Drain Charge	$V_{DS} = 10V, V_{GS} = 4.5V, I_{D} = 3A$	Q_{gd}		1.6		nC
Turn-On Delay Time	V_{DD} = 10V, V_{GEN} = 4.5V, I_D = 1A, R_G =6 Ω	t _{d(on)}		8		nS
Turn-On Rise time	V_{DD} = 10V, V_{GEN} = 4.5V, I_D = 1A, R_G =6 Ω	t _r		9		nS
Turn-Off Delay Time	V_{DD} = 10V, V_{GEN} = 4.5V, I_{D} = 1A, R_{G} =6 Ω	t _{d(off)}		15		nS
Turn-Off Fall time	$V_{DD} = 10V$, $V_{GEN} = 4.5V$, $I_{D} = 1A$, $R_{G} = 6\Omega$	t _f		4		nS

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS						
PARAMETER CONDITIONS SYMBOL MIN TYP. MAX UNIT						
Diode forward Voltage (Note 3)	I _S = 1.7A, V _{GS} = 0V	V_{DS}			1.2	V

Note:

- 1. Repetitive rating: Pulse width limited by junction temperature.
- 2. Surface mounted on FR4 board, t≤10s.
- 3. Pulse Test: Pulse Width≤80µs, Duty Cycle≤0.5%.
- 4. Guaranteed by design, not subject to production.



TYPICAL DEVICE RATING AND CHARACTERISTICS CURVES (TA = 25 °C unless otherwise noted)

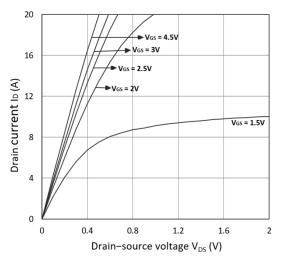


Fig.1 OUTPUT CHARACTERISTICS

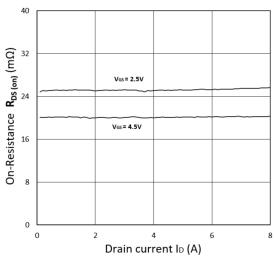


Fig.3 R_{DS(ON)} vs. I_D

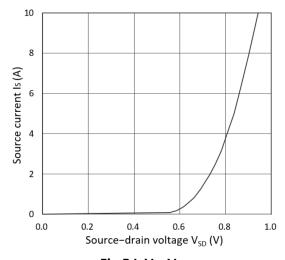


Fig.5 I_s Vs. V_{SD}

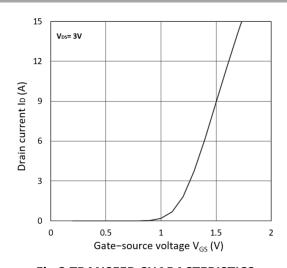


Fig.2 TRANSFER CHARACTERISTICS

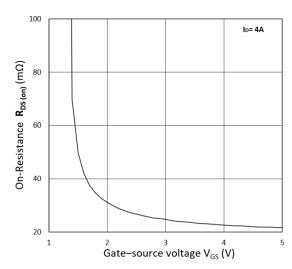


Fig.4 R_{DS(ON)} vs. V_{GS}

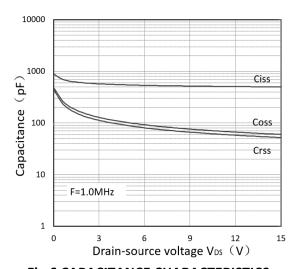


Fig.6 CAPACITANCE CHARACTERISTICS



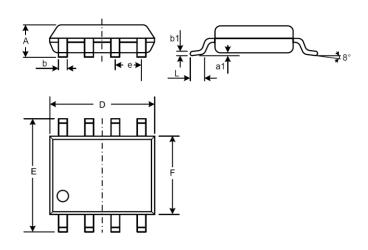
PINNING INFORMATION

PIN	SIMPLIFIED OUTLINE	CIRCUIT DIAGRAM
Pin1 Source 2 Pin2 Gate 2 Pin3 Source 1 Pin4 Gate 1 Pin5 Drain 1 Pin6 Drain 1 Pin7 Drain 2 Pin8 Drain 2	8 7 6 5	D2 D2 D1 D1 ********************************



PACKAGE INFORMATION

SOP-8L

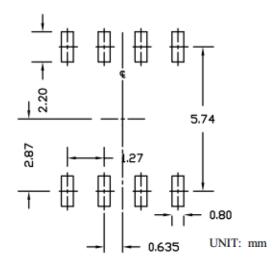


OUTLINE DIMENSIONS						
CVMAROL	MILLIMETERS		INC	HES		
SYMBOL	MIN	MAX	MIN	MAX		
А	1.23	1.75	0.048	0.069		
a1	0.05	0.25	0.002	0.010		
b	0.31	0.51	0.012	0.020		
b1	0.16	0.25	0.006	0.010		
D	4.70	5.15	0.185	0.203		
E	5.75	6.25	0.226	0.246		
e	1.07	1.47	0.042	0.058		
F	3.70	4.10	0.146	0.161		
L	0.40	1.27	0.016	0.050		

Notes

- 1. Dimensioning and tolerances per ANSI Y14.5M, 1985.
- 2. Controlling Dimension: Inches
- 3. Dimensions are exclusive of mold flash and metal burrs.

SOLDERING PAD

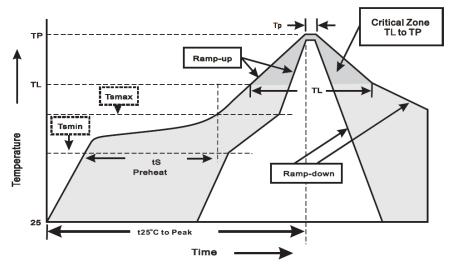




SOLDERING PARAMETERS

SUGGESTED THERMAL PROFILES FOR SOLDERING PROCESSES

- 1. Storage environment: Temperature=5 °C~40 °C Humidity=55% ±25%
- 2. Reflow soldering of surface-mount devices



3. Reflow soldering

PROFILE FEATURE	SOLDERING CONDITION
Average ramp-up rate (T _L to T _P)	<3 °C/sec
Preheat	
- Temperature Min (T _{smin})	150 °C
- Temperature Max (T _{smax})	200 °C
- Time (min to max) (t _s)	60 ~ 120 sec
T_{smax} to T_L	
- Ramp-upRate	<3 °C/sec
Time maintained above:	
- Temperature (T _L)	217 °C
- Time(tL)	60 ~ 260 sec
Peak Temperature (T _P)	255 °C-0/+5 °C
Time within 5 °C of actual Peak	10 ~ 30 sec
Temperature(tP)	
Ramp-down Rate	<6 °C/sec
Time 25 °C to Peak Temperature	<6 minutes



CUSTOMER NOTE:

DISCLAIMER

The product information and the selection guide facilitates the selection of the ALPINESEMI™'s Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review the Data sheet(s) so as to confirm that the Device(s) meets functionality parameters for your application. The information furnished on the Data Sheet and the ALPINESEMI™'s Web Site is believed to be accurate and reliable at the time of preparation of this document. ALPINESEMI™ however, does not assume any inaccuracies that may arise when the components are mounted and removed. Furthermore, ALPINESEMI™ does not assume liability whatsoever, arising out of the application or the use of any of ALPINESEMI™'s product(s). Neither, does it convey any license under its patent rights nor the rights of others. These products are not guaranteed for use in life saving/support appliances or systems. ALPINESEMI™'s customers using these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and ALPINESEMI™ will not be responsible in any way(s) for any damage(s) resulting from such use.

Please check the website www.alpinesemi.com for continues updates and revision of datasheets.

DESIGN CHANGES: ALPINESEMI™ strives for continuous improvement and reserves the right to change the specifications of its products without prior notice. ALPINESEMI™ reserves the right to discontinue product lines without prior notice. Any product selection is a recommendation based on best understanding of such product(s) by our engineers. However, buyers are advised to rely on their own judgment for such selection of the products.

ALPINESEMI™ makes no warranty, representation or guarantee regarding the suitability of its products for any particular applications. Neither does ALPINESEMI™ assume any liability arising out of the applications nor the use of such products. ALPINESEMI™ specifically disclaims all liabilities either consequential or incidental.

All rights of the product and datasheet are reserved to ALPINESEMI™.

All logos and information provided in the datasheets are for reference only. Any registered and/or trademark/logos belonging to respective companies be the property of those companies. ALPINESEMI™ extends the courtesy to them, if any of the information found in its datasheet.

Component Disposal Instructions

- 1. ALPINESEMI™ Semiconductor Devices are RoHS compliant and hence customers are requested to dispose as per the prevailing Environmental Legislation put forth in their specific country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).



sales@alpinesemi.com www.alpinesemi.com